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FAX

To:	Company	Fax #:	Telephone #:
 Examiner Nguyen 	Group Art Unit 2815	(703) 872-9317	(703) 308-1269
2)		746-6880	(100)000 120)
3)			
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INTERNATIONAL PHONE NU	MBERS MUST INCLUDE COUNTRY & CIT	CODE SECTORAL MANAGER PAGE	

No. of Pages: From: Jerome W. Massie Date: January 13, 2003 Application No: 09/736,230 (including this page) RECEIVED Comments:

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JAN 1 3 2003

Examiner Nguyen;

Technology Center 2800 Per our brief discussion today, please find attached a proposed Amendment Under 37 C.F.R. 1.116 for the above identified application. If convenient, I would like a brief

interview to discuss the draft amendment prior to the due date of February 27, 2003. If any portion of this transmission is not received, immediately contact me in order that a complete response can be forwarded to you. If you have any questions or comments, please do not hesitate to contact me. Thank you very much for your assistance in this matter.

Jerome Massie (703) 770-9362

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From: Jerome W. Massie	Date: January 13, 2003	No. of Pages: (including this page)	Client/Matter: 740819-474
User #: 2039	Ext: 9362	Disbursement Amount: 5	

Docket No. 740819-474

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
Masaaki NODA et al.		Group Art Unit: 2815
Serial No 09/736,230)	
Filed: December 15, 2000)	Examiner: Joseph H. Nguyer
For: HIGH-VOLTAGE SEMICONDUCTOR DEVICE)	PROPOSED

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on January ____, 2003.

Adele Stamper

AMENDMENT UNDER 37 C.F.R. 1.116

BOX AF

Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the final Office Action mailed November 27, 2002, please amend the above-identified patent application as follows:

In the Specification:

Please replace the paragraph spanning pages 13 and 14, beginning "An inventive high-voltage..." with the following:

-- An inventive high-voltage semiconductor device includes:

semiconductor substrate of a first conductivity type; semiconductor region of a second conductivity type; source and drain regions of the second conductivity type; body region of the first conductivity type; gate insulating film; gate electrode; field insulating film;

NVA251251,1